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PATENT NUMBER and
ISSUE DATE
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U.S. UTILITY Patent Application

APPL NUM 10039138	FILING DATE 12/31/2001	CLASS 439	SUBCLASS 92	GAU 2833	EXAMINER GILMAN
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**APPLICANTS: Bloomfield Rome; Fester Joseph;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO H052617.1141US0
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiner's initials		
TITLE : Method and apparatus for grounding a processor board		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims
			Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figa.Drwg.
TERMINAL DISCLAIMER		Print Fig.	
		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
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